

09/980509 #2

JC10 Rec'd PCT/PTO 19 OCT 2001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

*P. Br* 3/7/02  
Attorney Docket No. 37802PCT-US

In re Application of:  
**McCord, Don**

Serial No. **New Application**

Filed: **Herewith**

For: **Method and System for Wafer  
and Device-Level Testing of an  
Integrated Circuit**

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Examiner: **Unknown**

Group Art Unit: **Unknown**

**INFORMATION DISCLOSURE STATEMENT**  
**PURSUANT TO 37 C.F.R. § 1.97(b)**

Assistant Commissioner for Patents  
Washington, D. C. 20231

Sir:

Applicants request that the information listed on the attached Substitute Form PTO-1449 be considered by the Office during the pendency of the above-entitled application, pursuant to 37 C.F.R. 1.97. This Information Disclosure Statement is being filed under 37 C.F.R. § 1.97(b) within three months of the filing date of the application, or before the mailing date of a first office action on the merits. No fee or certification is required.

**CERTIFICATE OF MAILING BY "EXPRESS MAIL" UNDER 37 CFR § 1.10**

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I hereby certify that the application/correspondence attached hereto is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to Box: Patent Application, Assistant Commissioner for Patents, Washington, D.C. 20231.

*KIM DE LEON*

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*Kim De Leon*

Signature of person mailing paper or fee

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No fee is believed to be necessary; however, in the event that any additional fee is required, please charge it to **Bracewell & Patterson Deposit Account Number 50-0259**.

Respectfully submitted,

  
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